

AMENDMENT TO THE CLAIMS:

1-31. (Canceled)

32. (New) An electroluminescence display device, comprising:

a plurality of display pixels arranged in a matrix within a display pixel region on a substrate, said display pixels having electroluminescence elements including an emissive layer between first and second electrodes and a storage capacitor for storing display data for controlling power to be supplied to the electroluminescence elements; wherein

within said display pixel region, power source lines for supplying power from a power source to said electroluminescence elements are disposed in a grid pattern extending in row and column directions of the matrix, said power source lines intersect at a position between said display pixels.

33. (New) The device defined in Claim 32, wherein said power source lines include main power source lines arranged in plural numbers within said display pixel region, and at least one bypass power source line extended to intersect and connect said main power lines within said display pixel region.

34. (New) The device defined in Claim 33, wherein each of said display pixels further comprises:

a first thin film transistor having a gate electrode connected to a gate line, and a first electrode region connected to a data line; and

a second thin film transistor having a gate electrode connected to a second electrode region of said first thin film transistor, a first electrode region connected to one of said main power source lines, and a second electrode region connected to said electroluminescence element.

35. (New) The device defined in Claim 34, wherein said first and said second thin film transistors include active layers composed of poly-silicon.

36. (New) The device defined in Claim 32, wherein said power source lines include main power source lines arranged in plural numbers along the column direction of said matrix within said display pixel region, and at least one bypass power source line extended in the row direction of said matrix within said display pixel region to intersect and connect said main power lines.

37. (New) The device defined in Claim 36, wherein each of said display pixels further comprises:

a first thin film transistor having a gate electrode connected to a gate line, and a first electrode region connected to a data line; and

a second thin film transistor having a gate electrode connected to a second electrode region of said first thin film transistor, a first electrode region connected to one of said main power source line, and a second electrode region connected to said electroluminescence element.

38. (New) The device defined in Claim 37, wherein said first and said second thin film transistors include active layers composed of poly-silicon.

39. (New) The device defined in Claim 36, wherein said main power source lines and said bypass power source line are conductive line integrally formed.

40. (New) The device defined in Claim 36, wherein said main power source lines and said bypass power source line are conductive lines separately formed in different processes.

41. (New) The device defined in Claim 32, wherein said emissive layer is a layer using an organic compound as an emissive material.

42. (New) An electroluminescence display device, comprising:

a display pixel region having a plurality of display pixels arranged in a matrix, wherein each of said display pixels includes:

an electroluminescence element having an emissive layer between an anode and a cathode;

a first thin film transistor having a gate electrode connected to a gate line, and a first electrode region connected to a data line; and

a second thin film transistor having a gate electrode connected to a second electrode region of said first thin film transistor, a first electrode region connected to power source lines for supplying power from a power source to said electroluminescence element, and a second electrode region connected to said electroluminescence element, said power source lines intersect at a position between said display pixels; and

a storage capacitor having a first capacitor electrode electrically connected to the gate electrode of the second thin film transistor and to the second electrode region of the first thin film

transistor and a second capacitor electrode formed opposing the first capacitor electrode having an insulating film interposed between the first and second capacitor electrodes; wherein

said power source lines are provided along a column direction of said matrix within said display pixel region, and those power source lines that are associated with the display pixels adjacently arranged along a row direction are connected to one another by a bypass power source line extending in the row direction of said matrix.

43. (New) The device defined in Claim 42, wherein said first and said second thin film transistors include active layers composed of poly-silicon.

44. (New) The device defined in Claim 42, wherein said main power source lines and said bypass power source line are conductive line integrally formed.

45. (New) The device defined in Claim 42, wherein said main power source lines and said bypass power source line are conductive lines separately formed in different processes.

46. (New) The device defined in Claim 42, wherein said emissive layer is a layer using an organic compound as an emissive material.

47. (New) An emissive display device comprising:

a plurality of display pixels arranged in a matrix within a display pixel region, each of said display pixels having emissive elements including an emissive layer between first and second electrodes and a storage capacitor for storing display data for controlling power to be supplied to the emissive elements; wherein

within said display pixel region, power source lines for supplying power from a power source to said emissive elements are disposed in a grid pattern extending in row and column directions of the matrix, said power source lines intersect at a position between said display pixels.

48. (New) The device defined in Claim 47, wherein said power source lines include a main power source lines arranged in plural numbers along the column direction of said matrix within said display pixel region, and at least one bypass power source line extended in the row direction of said matrix within said display pixel region to intersect and connect said main power lines.

49. (New) The device defined in Claim 48, wherein each of said display pixels further comprises:

a first thin film transistor having a gate electrode connected to a gate line, and a first electrode region connected to a data line; and

a second thin film transistor having a gate electrode connected to a second electrode region of said first thin film transistor, a first electrode region connected to one of said main power source lines, and a second electrode region connected to said emissive element.

50. (New) The device defined in Claim 49, wherein said first and said second thin film transistors include active layers composed of poly-silicon.

51. (New) The device defined in Claim 48, wherein said main power source lines and said bypass power source line are conductive line integrally formed.

52. (New) The device defined in Claim 48, wherein said main power source lines and said bypass power source line are conductive lines separately formed in different processes.

53. (New) The device defined in Claim 47, wherein said emissive layer is a layer using an organic compound as an emissive material.

54. (New) An electroluminescence display device, comprising:

a plurality of display pixels arranged in a matrix within a display pixel region on a substrate, said display pixels having electroluminescence elements each having a layered structure constructed by forming an anode and a cathode with at least an emissive layer formed therebetween; and

a storage capacitor for storing display data for controlling power to be supplied to the electroluminescence elements,

wherein within said display pixel region, power source lines for supplying power from a power source to said electroluminescence elements are disposed in a grid pattern extending in row and column directions of the matrix,

wherein the power source lines are selectively and electrically connected to either said anode or said cathode of said electroluminescence element to supply power to connected one of said anode or said cathode.

55. (New) An electroluminescence display device, comprising:
a display pixel region having a plurality of display pixels arranged in a matrix, wherein each of said display pixels includes:
an electroluminescence element having an emissive layer between an anode and a cathode;
a first thin film transistor having a gate electrode connected to a gate line, and a first electrode region connected to a data line; and
a second thin film transistor having a gate electrode connected to a second electrode region of said first thin film transistor, a first electrode region connected to power source lines for supplying power from a power source to said electroluminescence element, and a second electrode region connected to said electroluminescence element;
a storage capacitor having a first capacitor electrode electrically connected to the gate electrode of the second thin film transistor and to the second electrode region of the first thin film transistor and a second capacitor electrode formed opposing the first capacitor electrode having an insulating film interposed between the first and second capacitor electrodes;
wherein said power source lines are provided along a column direction of said matrix within said display pixel region, and said power source lines that are associated with the display pixels adjacently arranged along a row direction are connected to one another by a bypass power source line extending in the row direction of said matrix,
wherein said power source lines are selectively and electrically connected to either said first electrode or said second electrode of said electroluminescence element to supply power to said first electrode or said second electrode.

56. (New) An emissive display device comprising:
a plurality of display pixels arranged in a matrix within a display pixel region, each of said display pixels having emissive elements each having a layered structure constructed by forming an anode and a cathode with at least an emissive layer formed therebetween;
a storage capacitor having a first capacitor electrode electrically connected to the gate electrode of the second thin film transistor and to the second electrode region of the first thin film transistor and a second capacitor electrode formed opposing the first capacitor electrode having

an insulating film interposed between the first and second capacitor electrodes;wherein

within said display pixel region, power source lines for supplying power from a power source to said emissive elements are disposed in a grid pattern extending in row and column directions of the matrix,

wherein said power source lines are selectively and electrically connected to either said anode or said cathode of said emissive elements to supply power to connected one of said anode or said cathode.